



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Robert B. Staszewski, et al.

Serial No.: **09/695,516**

Filed: **10/24/2000**

For: **HYBRID OF PREDICTIVE AND CLOSED-LOOP PHASE DOMAIN DIGITAL PLL ARCHITECTURE**

Docket No.: **TI-30674**

Examiner: **Kinkead**

Art Unit: **2817**

Confirm. No.: **3115**

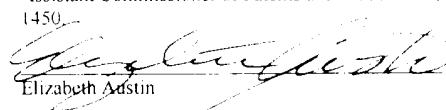
AMENDMENT - 37 C.F.R. § 1.111

Assistant Commissioner for Patents

Washington, D.C. 20231

MAILING CERTIFICATE UNDER 37 CFR § 1.8(a)

I hereby certify, that on this date, this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner of Patents and Trademarks, Alexandria, VA 22313-1450.


Elizabeth Austin

5/20/03
Date

Dear Sir:

Responsive to the Office Action dated December 24, 2002, please amend the above-identified application as follows:

IN THE CLAIMS - (clean copy):

12. (twice amended) A phase-locked loop system comprising:
a digitally-controlled oscillator responsive to an oscillator tuning word (OTW) to generate

to the OTW:

TECHNOLOGY CENTER 2800
MAY 27 2003